



TRANSMITTAL FORM (to be used for all correspondence after initial filing)		Application No.	09/651,385
		Filing Date	August 29, 2000
		First Named Inventor	Sanjay Dabral
		Group Art Unit	2815
		Examiner Name	Jose R. Diaz
Total Number of Pages in This Submission	10	Attorney Docket Number	42390P5258D

RECEIVED
AUG -7 2000
TECHNOLOGY CENTER 2800

ENCLOSURES (check all that apply)		
<input checked="" type="checkbox"/> Fee Transmittal Form <input type="checkbox"/> Fee Attached <input checked="" type="checkbox"/> Amendment / Response <input checked="" type="checkbox"/> After Final <input type="checkbox"/> Affidavits/declaration(s) <input type="checkbox"/> Extension of Time Request <input type="checkbox"/> Express Abandonment Request <input type="checkbox"/> Information Disclosure Statement <input type="checkbox"/> Certified Copy of Priority Document(s) <input type="checkbox"/> Response to Missing Parts/ Incomplete Application <input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53	<input type="checkbox"/> Assignment Papers (for an Application) <input type="checkbox"/> Drawing(s) <input type="checkbox"/> Licensing-related Papers <input type="checkbox"/> Petition <input type="checkbox"/> Petition to Convert a Provisional Application <input type="checkbox"/> Power of Attorney, Revocation Change of Correspondence Address <input type="checkbox"/> Terminal Disclaimer <input type="checkbox"/> Request for Refund <input type="checkbox"/> CD, Number of CD(s)	<input type="checkbox"/> After Allowance Communication to Group <input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences <input type="checkbox"/> Appeal Communication to Group (Appeal Notice, Brief, Reply Brief) <input type="checkbox"/> Proprietary Information <input type="checkbox"/> Status Letter <input checked="" type="checkbox"/> Other Enclosure(s) (please identify below): <div style="border: 1px solid black; padding: 5px; margin-top: 10px;">Return Postcard</div>
Remarks		

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT	
Firm or Individual name	Raul D. Martinez, Reg. No. 46,904 BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP
Signature	<i>Raul D. Martinez</i>
Date	July 25, 2002

CERTIFICATE OF MAILING/TRANSMISSION			
I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class mail with sufficient postage in an envelope addressed to: Box AF, Assistant Commissioner for Patents, Washington, D.C. 20231 on: July 25, 2002			
Typed or printed name	Lillian E. Rodriguez		
Signature	<i>Lillian E. Rodriguez</i>	Date	July 25, 2002



COPY OF PAPERS
ORIGINALLY FILED

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

PTO/SB/17 (11-01)
Approved for use through 10/31/2002. OMB 0651-0032
U.S. Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

FEE TRANSMITTAL 1st FY 2002

Patent fees are subject to annual revision.

Complete if Known

Application Number	09/651,385
Filing Date	August 29, 2000
First Named Inventor	Sanjay Dabral
Examiner Name	Jose R. Diaz
Group/Art Unit	2815
Attorney Docket No.	42390P5258D

☐ Applicant claims small entity status. See 37 CFR 1.27.

TOTAL AMOUNT OF PAYMENT (\$) 0.00

METHOD OF PAYMENT (check one)

☐ Check ☐ Credit card ☐ Money Order ☐ Other ☒ None
☐ Deposit Account

Deposit
Account
Number

02-2666

Deposit
Account
Name

Blakely, Sokoloff, Taylor & Zafman LLP

The Commissioner is authorized to: (check all that apply)

☐ Charge fee(s) indicated below ☒ Credit any overpayments
☒ Charge any additional fee(s) required under 37 CFR §§ 1.16, 1.17, 1.18 and 1.20.
☐ Charge fee(s) indicated below, except for the filing fee to the above-identified deposit account

FEE CALCULATION

1. BASIC FILING FEE

Large Entity		Small Entity		Fee Description	Fee Paid
Fee Code	Fee (\$)	Fee Code	Fee (\$)		
101	740	201	370	Utility filing fee	
106	330	206	165	Design filing fee	
107	510	207	255	Plant filing fee	
108	740	208	370	Reissue filing fee	
114	160	214	80	Provisional filing fee	
SUBTOTAL (1)					(\$)

2. EXTRA CLAIM FEES

Total Claims	10	20	Extra Claims	Fee from below	Fee Paid
Independent Claims	1	3	0	84.00	\$0.00
Multiple Dependent					

Large Entity		Small Entity		Fee Description	Fee Paid
Fee Code	Fee (\$)	Fee Code	Fee (\$)		
103	18	203	9	Claims in excess of 20	
102	84	202	42	Independent claims in excess of 3	
104	260	204	140	Multiple Dependent claim, if not paid	
109	84	209	42	**Reissue independent claims over original patent	
110	18	210	9	**Reissue claims in excess of 20 and over original patent	
SUBTOTAL (2)					(\$) 0.00

**or number previously paid, if greater. For Reissues, see below

FEE CALCULATION (continued)

3. ADDITIONAL FEES

Large Entity		Small Entity		Fee Description
Fee Code	Fee (\$)	Fee Code	Fee (\$)	
105	130	205	65	Surcharge - late filing fee or oath
127	50	227	25	Surcharge - late provisional filing fee or cover sheet
139	130	139	130	Non-English specification
147	2,520	147	2,520	For filing a request for ex parte reexamination
112	920 *	112	920 *	Requesting publication of SIR prior to Examiner action
113	1,840 *	113	1,840 *	Requesting publication of SIR after Examiner action
115	110	215	55	Extension for reply within first month
116	400	216	200	Extension for reply within second month
117	920	217	460	Extension for reply within third month
118	1,440	218	720	Extension for reply within fourth month
128	1,960	228	980	Extension for reply within fifth month
119	320	219	160	Notice of Appeal
120	320	220	160	Filing a brief in support of an appeal
121	280	221	140	Request for oral hearing
138	1,510	138	1,510	Petition to institute a public use proceeding
140	110	240	55	Petition to revive - unavoidable
141	1,280	241	640	Petition to revive - unintentional
142	1,280	242	640	Utility issue fee (or reissue)
143	460	243	230	Design issue fee
144	620	244	310	Plant issue fee
122	130	122	130	Petitions to the Commissioner
123	50	123	50	Processing fee under 37 CFR 1.17(q)
126	180	126	180	Submission of Information Disclosure Stmt
581	40	581	40	Recording each patent assignment per property (times number of properties)
146	740	246	370	Filing a submission after final rejection (37 CFR § 1.129(a))
149	740	249	370	For each additional invention to be examined (37 CFR § 1.129(b))
179	740	279	370	Request for Continued Examination (RCE)
169	900	169	900	Request for expedited examination of a design application

Other fee (specify) _____

* Reduced by Basic Filing Fee Paid

SUBTOTAL (3)

(\$)

SUBMITTED BY

Complete (if applicable)

Name (Print/Type)	Raul D. Martinez	Registration No. (Attorney/Agent)	46,904	Telephone	(310) 207-3800
Signature	<i>Raul D. Martinez</i>	Date	07/25/02		

WARNING: Information on this form may become public. Credit card information should not be included on this form. Provide credit card information and authorization on PTO-2039.

Burden Hour Statement: This form is estimated to take 0.2 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231.



COPY OF PAPERS
ORIGINALLY FILED

Our Ref. No.: 42390P5258D

UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Sanjay Dabral and Krishna Seshan

Application No.: 09/651,385

Filed: August 29, 2000

For: DIODE AND TRANSISTOR DESIGN
FOR HIGH SPEED I/O

Examiner: Jose R. Diaz

Art Unit: 2815

TECHNOLOGY CENTER 2800

AUG - 7 2002

RECEIVED

9/4/02
Mullish

Box AF
Assistant Commissioner for Patents
Washington, D.C. 20231

AMENDMENT AND RESPONSE TO FINAL OFFICE ACTION

In response to the Final Office Action dated May 21, 2002, Applicants respectfully request entry of the amendments set forth below and consideration of the remarks that follow.

IN THE CLAIMS

Please substitute the following amended claims for the pending claims with the same numbers:

1 20. (Amended) A method of forming an integrated circuit comprising:
2 forming a performance circuit occupying a first well of an integrated circuit substrate;
3 forming a protection circuit occupying a second well of an integrated circuit substrate
4 separate from the first well; and
5 coupling the protection circuit to the performance circuit.

25. (Twice Amended) The method of claim 24, the doped region being a first doped
region of a first dopant in the second well of the substrate, the second well being doped with a first